BLT EXCELLENCE BY DESIGN

AMD Xilinx Recommended Learning Path

LANGUAGES

- Designing with VHDL
- Designing with Verilog
- Designing and Verification with SystemVerilog

CORE

- Vivado Boot Camp 1: FPGA Essentials
- Vivado Boot Camp 2: Tcl, IP and Debugging
- Vivado Boot Camp 3: Floorplan & Timing Closure

ADVANCED

- Embedded Systems Hardware Design Boot Camp
- Embedded Design with PetaLinux Tools
- Dynamic Function Exchange (DFX)

SPECIALTY

- Designing with an Integrated PCI Express System
- Kria SOM
- Vitis Model Composer (DSP)
- Designing with the Zynq UltraScale+ RFSoC
- High Level Synthesis with the Vitis HLS Tool

VERSAL CORE

- Designing with the Versal AI Engines 1
- Adaptive SoC Architecture and Methodology

INTERMEDIATE

- Designing with the Versal AI Engines 2

ADVANCED

- Designing with the Versal AI Engines 3

SPECIALTY

- Designing with the Versal Network on Chip
- Power & Board Design

AMD Authorized Training Provider