

Pricelist

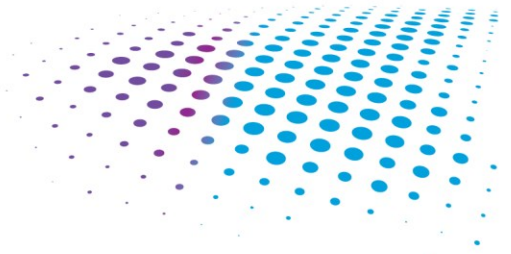
2022 Q2

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ITEM	COURSE DESCRIPTION	ONLINE PRICE (USD)	IN-PERSON PRICE (USD)
BLT-DEBUG1	Advanced Debug Techniques for Hardware Engineers	\$299.00	\$399.00
BLT-DSP-SYSGEN	Essential DSP Design Techniques using System Generator	\$897.00	\$1,197.00
BLT-EHWB	Embedded Systems Hardware Design Boot Camp	\$897.00	\$1,197.00
BLT-MGT-PCIE	Designing an Integrated PCI Express System with Xilinx Serial Transceivers	\$897.00	\$1,197.00
BLT-SV-DV	Designing and Verification with SystemVerilog	\$897.00	\$1,197.00
BLT-VIVB1	Vivado Boot Camp Phase-1: Designing for Performance	\$897.00	\$1,197.00
BLT-VIVB2	Vivado Boot Camp Phase-2: Implementing for Performance	\$897.00	\$1,197.00
BLT-VIVB3	Vivado Boot Camp Phase-3: Achieving Performance	\$897.00	\$1,197.00
BLT-VIV-Basic	Vivado Boot Camp: Basic Training	\$598.00	\$798.00
BLT-X4M2017	Xilinx for Managers	\$299.00	\$399.00
XLX-ACAP AIE1	Designing with Versal AI Engine 1: Architecture and Design Flow	\$598.00	\$798.00
XLX-ACAP AIE2	Designing with Versal AI Engine 2: Graph Programming with AI Engine Kernels	\$598.00	\$798.00
XLX-ACAP AIE3	Designing with Versal AI Engine 3: Kernel Programming and Optimization	\$598.00	\$798.00
XLX-ACAP ARCH	Designing with the Versal ACAP: Architecture and Methodology	\$897.00	\$1,197.00
XLX-ACAP NOC	Designing with the Versal ACAP: Network on Chip	\$299.00	\$399.00
XLX-ACAP POWER-BD	Designing with the Versal ACAP: Power and Board Design	\$299.00	\$399.00
XLX-AI ACCEL	Accelerating Applications with the Vitis Unified Software Environment	\$598.00	\$798.00
XLX-AI INFER	Developing AI Inference Solutions with the Vitis AI Platform	\$598.00	\$798.00
XLX-CONN MGT-US	Designing with UltraScale FPGA Transceivers	\$598.00	\$798.00
XLX-CONN PCIE	Designing an Integrated PCI Express System	\$598.00	\$798.00
XLX-CONN SI	Signal Integrity and Board Design for Xilinx FPGAs	\$897.00	\$1,197.00
XLX-CONN TRX	Designing with Xilinx Serial Transceivers	\$598.00	\$798.00
XLX-CONN-RFSOC	Designing with the Zynq UltraScale+ RFSoc	\$598.00	\$798.00
XLX-DSP ESS	Essential DSP Implementation Techniques for Xilinx FPGAs	\$299.00	\$399.00
XLX-DSP HLS	C-based Design: High-Level Synthesis with the Vivado HLx Tool	\$598.00	\$798.00
XLX-DSP SYSGEN	DSP Design Using System Generator	\$598.00	\$798.00

All course pricing is per student for public classes. Please call for private/on-site class pricing.

See page 2 for Additional Courses



Pricelist

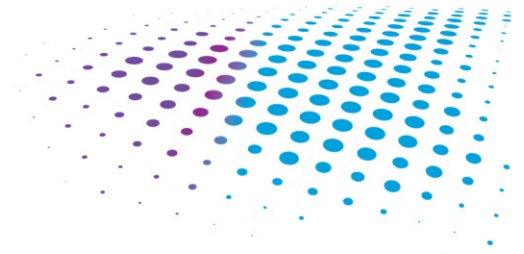
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ITEM	COURSE DESCRIPTION	ONLINE PRICE (USD)	IN-PERSON PRICE (USD)
XLX-EMBD PLNX	Embedded Design with PetaLinux Tools	\$598.00	\$798.00
XLX-EMBD ZUPSA	Zynq UltraScale+ MPSoC for the System Architect	\$598.00	\$798.00
XLX-EMBD ZUPSW	Zynq UltraScale+ MPSoC for the Software Developer	\$598.00	\$798.00
XLX-FPGA PR	Xilinx Partial Reconfiguration Tools & Techniques	\$598.00	\$798.00
XLX-FPGA US	Designing with the UltraScale and UltraScale+ Architectures	\$598.00	\$798.00
XLX-FPGA VDES1	Designing FPGAs Using the Vivado Design Suite 1	\$598.00	\$798.00
XLX-FPGA VDES2	Designing FPGAs Using the Vivado Design Suite 2	\$598.00	\$798.00
XLX-FPGA VDES3	Designing FPGAs Using the Vivado Design Suite 3	\$598.00	\$798.00
XLX-FPGA VDES4	Designing FPGAs Using the Vivado Design Suite 4	\$598.00	\$798.00
XLX-LANG ADV VHDL	Advanced VHDL	\$598.00	\$798.00
XLX-LANG SV DES	Designing with SystemVerilog	\$598.00	\$798.00
XLX-LANG SV VER	Verification with SystemVerilog	\$598.00	\$798.00
XLX-LANG VERILOG	Designing with Verilog	\$897.00	\$1,197.00
XLX-LANG VHDL	Designing with VHDL	\$897.00	\$1,197.00

All course pricing is per student for public classes. Please call for private/on-site class pricing.

See page 3 for Products and Services



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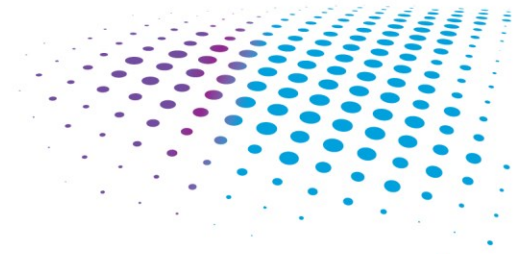
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ITEM	DESCRIPTION	PRICE EACH (USD)
BLT-ASP-4	ASP Phase - 4 week	\$20,000.00
BLT-ASP-6	ASP Phase - 6 week	\$30,000.00
BLT-ASP-8	ASP Phase - 8 week	\$40,000.00
BLT-ASP-10	ASP Phase - 10 week	\$50,000.00
BLT-ASP-12	ASP Phase - 12 week	\$60,000.00
BLT-IS-SAS	Includes: BLT-IP-SAT, BLT-ILT-SAT-B-P, BLT-SS-E-U	\$45,000.00
BLT-IP-SAT	SoC Acceleration Toolkit	\$20,000.00
BLT-ILT-B-SAT-P	SoC Acceleration Toolkit Training	\$15,000.00
BLT-SS-E-U	SoC Acceleration Integration Support (Unclassified)	\$15,000.00
BLT-HPP-1305-KZV-SFP		\$47,499.00
BLT-HPP-1305-KZ-SFP		\$37,499.00
BLT-HPP-1305-K-SFP		\$22,499.00
BLT-HPP-1305-KZV-FMC		\$46,999.00
BLT-HPP-1305-KZ-FMC		\$36,999.00
BLT-HPP-1305-K-FMC		\$21,999.00
BLT-HWP-IPB-3	Interposer Board	\$1,750.00
BLT-HWP-IPB-4	Interposer Board	\$2,100.00
BLT-HWP-ADC-FMC-5	ADC FMC	\$4,825.00
BLT-HWP-ADC-FMC-6	ADC FMC	\$12,312.50

See page 4 for Services



Excellence by Design



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DESCRIPTION OF SERVICES	HOURLY RATE (USD)
HARDWARE ENGINEERING*	
Subject Matter Expert 2 (SME 2)	\$333.33
Subject Matter Expert 1 (SME 1)	\$300.00
Senior Engineer	\$273.33
SOFTWARE ENGINEERING*	
Subject Matter Expert 2 (SME 2)	\$333.33
Subject Matter Expert 1 (SME 1)	\$300.00
Senior Engineer	\$273.33
MANAGEMENT AND ADMIN*	
Program Manager	\$198.67
Program Administrator	\$132.00

*Direct Rates / Commercial Pricing. Please contact BLT for Government and USG Prime Pricing.