BLT
BOTTOM LINE TECHNOLOGIES

Excellence by Design

Training ➞ Advice ➞ Support ➞ Design ➞ Production

FPGAs • SoCs • Boards • Systems

Aggressive Schedules • Rock Solid Service and Quality • High Performance

A Small Business Partner with a Big Reputation

ABOUT BLT

- Small Business
- An Engineering Design Services Firm
- Commercial, Industrial, Government, DoD/IC Clients
- Broad Technical Expertise and Experience with leading/bleeding edge technologies
- 30-year Track record of High Quality, Leading Edge, On-time Results

Design Center

Ellicott City, MD

Training Centers

Columbia, MD

Morristown, NJ

Sterling, VA

Feasterville-Trevose, PA

XILINX® CERTIFIED ALLIANCE PARTNER

✓ 320-point on-site audit of technical, business, quality, and support processes

✓ Rigorous certification testing including demonstrated expertise with the latest Xilinx® devices, tools and implementation techniques
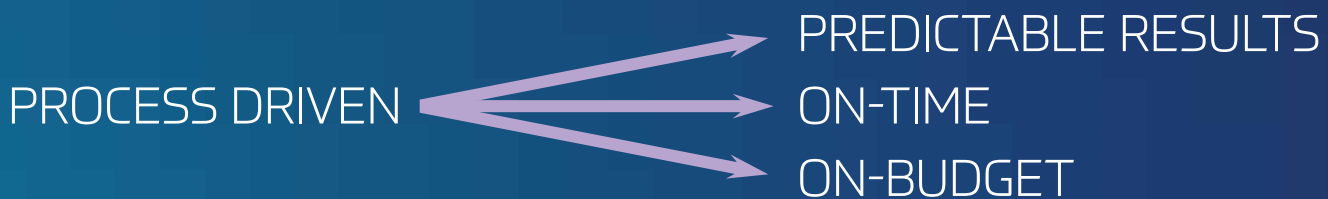
✓ Xilinx® Business and Technical Sponsorship

BLT'S ENGINEERS PASS THE SAME RIGOROUS TRAINING USED BY XILINX® FIELD APPLICATION ENGINEERS WORLDWIDE

888-XILINX-1
bltinc.com

ENGINEERING DESIGN SERVICES

A full range of hardware and embedded design services including
FPGAs, SoCs, PCB/CCAs, complete systems and roduction

- HDL Design
- High-speed Digital Design
- PCB Layout
- Analog Design
- C, HLS, MatLab Design
- Design for SWAPC

- Signal and Power Integrity
- RF Design
- Anti-Tamper
- Signal Processing
- Partial Reconfiguration
- Single Chip Cryptography

PROCESS DRIVEN ➤ PREDICTABLE RESULTS
➤ ON-TIME
➤ ON-BUDGET

EXCLUSIVE REGIONAL TRAINING PROVIDER

- Rochester, NY through Roanoke, VA
- Thousands of engineers trained
- FPGA/SoC, Language, Tool, Architecture, Connectivity, DSP

Training Services
- Special Events
- Bootcamps
- Dozens of Courses
- Management Training
- Public and Private (on-site/off-site) Classes
- Follow-on Coaching

XILINX

FPGAS AND SOCS

END-USER CUSTOMIZABLE CIRCUITRY & PROCESSORS

- Communications Systems
- Software Defined Radio
- Single Chip Cryptography (NSA Type-1)
- Signal Processing (RF/Video/Audio)

- Embedded Processing
- Supercomputing
- Reconfigurable computing
- Hi Rel/Rad Hard Applications

UP TO 1000 TIMES FASTER THAN SOFTWARE SOLUTIONS

Designs by BLT engineers or your engineers trained by BLT

- Shave weeks off each FPGA/SoC development schedule.
- Harness the full power of devices and tools and implement with a single command.
- Unlock the full potential of FPGAs/SoCs and achieve breakthrough performance.

Lower design and production costs

- Designs that run faster and use fewer silicon resources can use slower, smaller and **less expensive devices**.
- Avoid common mistakes and protracted schedules using BLT's time proven methodologies.

Up for a challenge? We're hiring!

- Full time Hardware & Software engineers
- Must be U.S. citizens and thrive being essential in a variety of technical challenges
- Contact HR@bltinc.com

"BLT training cut our design time by months. Sending our engineers to BLT training is now part of our design cycle."

"BLT took the time to understand things up front...They asked the right questions, understood the answers and delivered quality results."

"BLT has the experience and processes to avoid things that can trip you up. When they give you a schedule they're going to be able to meet it."

"I was impressed with BLT's transparency, integrity, and vested interest in my success....very collaborative."

888-XILINX-1

bltinc.com