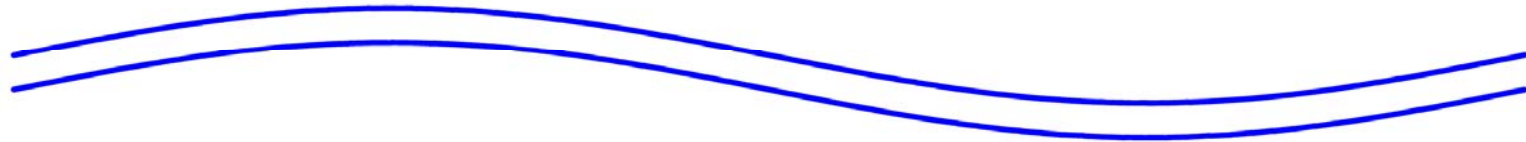


A Continuum of Solutions™



Training → Advice → Support → Design → Production

FPGAs • SoCs • Boards • Systems

Aggressive Schedules • Rock Solid Service and Quality • High Performance

A Small Business Partner with a Big Reputation

Bottom Line *Technologies*

A Continuum of Solutions

Training→Advice→Support→Design→Production

Who is Bottom Line?

Small Business

Commercial, Government, DoD/IC Clients

Broad Technical Expertise and Experience with leading/bleeding edge technologies

Spun off from Xilinx in 1989

Focus on Xilinx FPGAs, SoCs, SW Tools, and Design Methodologies

25 year Track record of High Quality, Leading Edge, On-time Results



Columbia MD Training Center

**888 – XILINX – 1
(888 – 945 – 4691)**



Westminster MD Design Center

BLT helped Xilinx® develop the Design Partner Program in 1990

Premier Members (0 Design Partners)	<u>Select providers</u> ✓ BLT 320 point on-site Audit of technical, business, quality, and support processes
Certified Members (Only 7 U.S. Design Partners)	<u>Certified level of FPGA expertise</u> ✓ BLT Corporate Self-Audit ✓ BLT Certification Testing Passed
Members	<u>Broad base of qualified providers</u> ✓ BLT XAP Application & Agreement ✓ BLT Business/Technical Sponsor

As a Certified Member BLT has

Demonstrated qualified expertise on the latest Xilinx devices and implementation techniques

BLT's engineers pass the same rigorous training used by Xilinx Field Application Engineers worldwide

As Xilinx's exclusive regional Authorized Training Provider, New Alliance candidates must be trained by BLT to become certified

Bottom
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Training Services



| AUTHORIZED TRAINING PROVIDER

BLT is Xilinx's EXCLUSIVE training resource from Rochester, NY through Roanoke, VA

MANY HUNDREDS of engineers Trained over 15 years

25 Language and FPGA classes

Public Classes

Private Classes (on-site / off-site)

BLT Professional Advisory Services

Most every engineer knows that decisions made (or not made) "up-front" usually set the tone and likelihood of a project's success and can have significant impact on schedules and budgets.

BLT Professional Advisory services are most often employed in the early stages of a design and best before entry has started. In addition to IP and device selection and utilization, typical involvements include providing recommendations and best practices for planning, architecting, designing, implementing and verifying designs.

BLT Professional Support Services

BLT Professional Support can be used when specific challenges or problems are encountered in the course of entering, implementing or verifying a design. BLT Professional Support provides reactive support that focuses on a specific problem, error message, or functionality that is not working as expected, desired or required.

Design Services

BLT designs FPGAs, SoCs, Boards, and Complete Systems

Commercial / Industrial / Medical / Military / IC

Low Power • High-Speed • High Density

Data and Telecommunications • Digital Signal Processing (DSP)

Embedded processors / ARM

Single Chip Cryptography • Partial Reconfiguration • High-Speed Signal Integrity

LARGE Designs? <ul style="list-style-type: none">• Multiple IP cores?• High I/O counts?• Multiple engineer teams?	FAST Designs? <ul style="list-style-type: none">• What are the BEST WAYS to consistently achieve reliable designs that run at 200, 300 and over 400 MHz?	Cost Sensitive Designs? <ul style="list-style-type: none">• Would fitting into smaller and/or slower parts help?
Inconsistent results? <ul style="list-style-type: none">• Sometimes it routes, sometimes it doesn't?• Sometimes meets timing, sometimes it doesn't?• Sometimes it simulates and routes but fails on the bench?	Tight Schedules? <ul style="list-style-type: none">• Concerned about issues that might come up late in the development schedule?• Pressured to compress schedules more than ever?	Tool Issues? <ul style="list-style-type: none">• Sometimes it seems the tools work against not with you?• Sometimes wonder what the tools are REALLY doing?• Last minute / simple changes break working designs?

BLT techniques allow our Clients to achieve breakthrough performance.

Designs by BLT and engineers taught by BLT typically shave weeks off each FPGA development schedule.

Designs by BLT and engineers taught by BLT are typically implemented with a single click. Can yours?

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Training and Engineering Clients

1553 SYSTEMS AAI ACCUSORT ADVANCED ACOUSTIC CONCEPTS AGFA AGILIS ALAN ORGAN ALLIED SIGNAL AEROSPACE TEST SYSTEMS DIVISION ALPHION AMERICAN SYSTEMS CORP AMP ANAMIR ELECTRONICS ASDI AT&T AT&T FREEHOLD AT&T HOLMDELL AT&T MICROELECTRONICS AT&T MIDDLETOWN AT&T MURRAY HILL AT&T WHIPPANY ATT LIBERTY CORNER AW COMPUTER AYDEN CONTROLS BAE SYSTEMS BELLCORE BENDIX BENDIX AEROSPACE BENDIX FLIGHT SYSTEMS BETTS BLONDER TONGUE BOEING BOONTON BSD BROADBAND BURL C-COR.NET CINCINNATI ELECTRONICS	CIRCADIANT SYSTEMS CMC COGNITRONICS COHERENT COMDIAL COMPUTER ENTRY COMTEX BAE SYSTEMS CONCURRENT CONKLIN CRYPTEK CVC DATA GENERAL DATAMEDIA DATASCOPE DATATEL DAVID SARNOFF RESEARCH DAYTRONIC DCX SYSTEMS DGM&S/TRILIUM DGT DIAGNOSTIC RETRIEVAL SYSTEMS DIALOGIC DIGILOG DIGITAL DEVICES DIGITAL VIDEO PROCESSING DoD DRANETZ DSPCON DTRC DUPONT DYNAMIC DIGITAL E-SYSTEMS EG&G ENCORE-GOULD ERICSSON/GE EUROTHERM	EXPONENT FEL FIBERCOM FISHER & PORTER FLAM&RUSSEL FORMATION GAMRY INSTRUMENTS GBC GE ASTRO GE SALEM GE-LYNCHBURG GE-OCEAN SYSTEMS GE/RCA CAMDEN GENERAL DYNAMICS GENICOM GIBRALTER GLOBAL ONE HAMILTON TECH HEKIMIAN HILLIER HONEYWELL-FORT WASHINGTON HONEYWELL-YORK HP HRB SINGER HTS HUGHES NW HURLEY HYPERCHIP IBM ICC ICI IMAGE CIRCUITS INC INFOTRON INFRARED OF NEW JERSEY INTEGRATED NW INTERDIGITAL ISR	ITT JERROLD JOHN BEAL JOHNS HOPKINS / APL KARDIOS KODAK KODEOS COMMUNICATIONS KOLLSMAN KULIC & SOPHA LITTON LOCKHEED MARTIN LUCENT DIGITAL RADIO LUCENT TECHNOLOGIES MADRIGAL AUDIO LABS MAGNAVOX MARTIN MARIETTA MARYLAND PROCUREMENT MATRIX METRAPLEX MICRO-PROSE MICROCOM MICROPROSE NASA NAVAL ORDANANCE STATION NER NETRIX O'NEIL COMMUNICATIONS NORTEL NORTHROP GRUMMAN CORP PACIFIC SCIENTIFIC PAR PARAGON NETWORKS INTERNATIONAL PEERLESS INSTRUMENTS PENRIL PERFORMANCE TECH PHILLIPS PLESSEY	PROBJECT PULSECOM REDCOM SAIC SARNOFF LABS SIRIUS SATELLITE RADIO SARNOFF REAL TIME SYSTEMS SIERRA SMARTDISK SMARHOUSE SMITH INDUSTRIES SOPHA SPERRY MARINE SPEX SPIRENT STANFORD TELECOM TELECOM ANALYSIS SYSTEMS TIMEPLEX TELEDRIILL TECHNICALLY SPEAKING TIMEPLEX TRIPLEX SYSTEMS TRANSLUX TTC TVL UNISYS UNISYS-DEVON UNISYS-FLEMINGTON US ARMY US NAVY VISTA RESEARCH VERSITRON VERTIS VILLAGE NETWORKS WELCH-ALAN WESTINGHOUSE XILINX ZYTRON CONTROL PRODUCTS
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Digital circuitry on a chip

Communications Systems

Software Defined Radio

Single Chip Cryptography (NSA Approved Type-1)

ULTRA secure applications

Signal Acquisition

Signal Processing (RF / Video / Audio)

Embedded Processing (2000 DSP Elements / Device)

Supercomputing

Reconfigurable computing

Capable of supporting Hi Rel / Rad Hard Applications

1000+ times faster than software solutions